

FORM PTO-1449  
(Equivalency)U.S. Department of Commerce  
Patent and Trademark OfficeU.S. Application Serial No.  
09/882,323Atty. Docket No.  
AM-2772.P1INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Ajay Kumar et al.  
ApplicantsJune 13, 2001  
Filing Date1763  
Group

## U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Issue Date	Name	Class	Subclass	Filing Date If Appropriate
<u>JAC</u>	Re. 30,505	02/03/81	Jacob	156	643	
	4,615,764*	10/07/86	Bobbio et al.	156	643	
	4,793,897	12/27/88	Dunfield et al.	156	643	
	5,211,790	05/18/93	Tatsumi	156	662	
	5,362,361	11/08/94	Tatsumi	156	662	
	5,397,431	03/14/95	Kadomura	156	662	
	5,721,090*	02/24/98	Okamoto et al.	430	313	
	5,753,418	05/19/98	Tsai et al.	430	313	
	5,779,926*	07/14/98	Ma et al.	216	67	
	5,817,579	10/06/98	Ko et al.	438	740	
	5,910,392*	06/08/99	Nozaki et al.	430	270.1	
<u>AAC</u>	5,911,887*	06/15/99	Smith et al.	216	72	

Examiner

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- Cited by the Examiner in the parent application.



### U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Issue Date	Name	Class	Subclass	Filing Date If Appropriate
<i>her</i>	6,080,678	06/27/00	Yim	438	725	
<i>her</i>	6,110,826*	08/29/00	Lou et al.	438	674	
<i>her</i>	6,132,631*	10/17/00	Nallan et al.	216	16	

### FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Publication Date	Name	Class	Subclass	Translation If Appropriate
<i>her</i>	WO 9616437	05/30/96	Chen	H01L	21/311	

### OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

*her* W. R. Runyan and K. E. Bean, "Semiconductor integrated circuit processing technology," Addison-Wesley Publishing Company, pp. 268 - 280 (1990).

*her* U.S. Patent Application Serial No. 08/907,448, of Nallan et al.

Examiner *Ma Olsen* Date Considered

*12/18/03*

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- \* Cited by the Examiner in the parent application.